

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Xisheng Zhang et al.
Assignee: Cadence Design Systems, Inc.
Title: MOSFET Modeling for IC Design Accurate for High Frequencies
Application No.: Unknown Filing Date: Herewith
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Docket No.: BTAT.001US1 Conf. No.: Unknown

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Dear Sir:

Please preliminary amend the present application prior to examination as follows:

BTAT.001US1

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